



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/706,841

Applicant : Franz Hofmann, et al. Filed : November 12, 2003

Art Unit : 2818

Title : Method for Manufacturing a Multi-Bit Memory Cell

Docket No. : P2000,0149 D

Customer No. : 24131

## STATUS LETTER

Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Sir:

It is respectfully requested that this Status Letter be returned to:

LERNER AND GREENBERG, P.A.

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with an indication as to when an action may be expected.

Respectfully submitted,

Alexia J. Vrahimis

Date: January 24, 2005

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